Costs entailed by software failures demonstrate that the systematic development of software in a certain quality is still a challenge, even after decades of research. A reason for this can certainly be found within the individual projects. Often, known techniques of quality assurance are not well understood and properly used due to deadline and budget restrictions. However, another reason is also the lack of techniques for quality assurance for specific types of software and software developed according to specific programming and modeling techniques.

Many ad-hoc security specifications, models, and protocols, which were implemented in software components in the past, turned out to be vulnerable to some extent. Formal verification and validation methodologies have the potential to increase user confidence in software artifacts. Therefore, theoretical foundations for security assurance should be investigated to discover new methods that will bring high certainty to the trustworthiness of software entities.

Verification and validation (V&V) methods, especially testing, deliver important analytical techniques for quality assurance. In model-based V&V, the software under consideration is considered by means of a model that focuses on certain aspects, often on the behavior of the software. Models frequently used are finite state machines and flow graphs. Testing techniques based on such models are, for instance, formal verification, control and data flow analysis, test case generation, and model checking.

This workshop aims at giving researchers and practitioners a platform to present their results and experience to a broader audience.

The topics of interest include, but are not limited to:

- Formal methods and theories in model-based V&V
- Simulation by models, forecasts of behavior and properties by models
- Models and modeling notations for programming and V&V
- Tools for model-based V&V
- Model-based security evaluation
- V&V of security specifications, models, and protocols
- Theoretical foundations of security analysis and design
- Formal models for security testing
- Testing with software usage models
- Test case generation based on formal and semi-formal models
- Test coverage metrics and criteria for model-based testing
- Models as test oracles, test validation with models
- Application of model checking in testing
- Model-based V&V of reactive and object-oriented systems
- Model-based verification and validation of tests
- Experience reports and requirements from model-based V&V and model-based development in practice

**Desired length of the workshop**

A one-day workshop held before the main conference.

**Important dates**

April 29: submission deadline *(extended)*
May 25: notification date
June 10: camera-ready
Workshop Proceedings, Paper Presentation (tentative)

Accepted submissions will be published by IEEE Press and available in the IEEE digital library. For this, one of the authors needs to register and present the accepted submission. During the workshop, each paper will have a slot of 30 minutes, with 20-25 minutes presentation.

Organizers

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Tugkan Tuglular completed his PhD in information security policies at Ege University (Turkey) in 1999. During his PhD, he worked as a research associate at Purdue University (USA) in the COAST Laboratory (currently CERIAS) for 16 months. After a brief period as a teaching associate at Ege University, he became assistant professor at the Izmir Institute of Technology (Turkey) in 2000. He was the CIO of the Izmir Institute of Technology from 2003 until 2007. He has interest and experience in information/network security and specifically in model-based testing and test automation of security software. Visit http://www.iyte.edu.tr/~tugkantuglular for publications.

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Fevzi Belli completed his PhD in formal methods for self-correction features in formal languages at Berlin Technical University in 1978. He spent several years as a software engineer in Munich, writing programs to test other programs. After a brief stint as a senior scientist at the SHAPE Technical at The Hague (NL), he became in 1983 a professor at the University of Applied Sciences in Bremerhaven; in 1989 he changed to the University of Paderborn. Prior to his appointment with the University of Paderborn, Dr. Belli was also a faculty member of the University of Maryland, College Park, European Division, for many years. During 2002 and 2003, he was founding chair of the Computer Science Department at the University of Economics in Izmir, Turkey. Currently, he is professor at Izmir Institute of Technology. He has an interest and experience in software reliability/fault tolerance, model-based testing, and test automation, and published more than 120 papers and 12 books in these areas. He was general chair and program chair of and invited speaker at numerous conferences on those topics, organized by IEEE and ACM, e.g., COMPSAC 2007, ISSRE 1998, ISSRE 2001, IEA-AIE 1992, and many more. Visit http://quebec.upb.de/personal.php?id=1 for publications.

Programme Committee (preliminary, not yet contacted)

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