The 3rd IEEE International Workshop on Hardware Software Co-Design

in conjunction with

QRS2016

Vienna, Austria, August 1-3, 2016

Hardware Software Co-Design (HSCD) is a basic methodology of embedded system which often is used in life critical situation, where reliability and safety are more important criteria than performance. Hardware software co-design usually means meeting system-level objectives by exploiting the synergism of hardware and software through their concurrent design. Co-design problems have different flavors according to the application domain, implementation technology and design methodology.

The HSCD workshop provides a forum to bring together researchers and practitioners working towards improving the Hardware and Software Co-Design and their related areas. The workshop focuses on innovative methodologies, techniques, tools, management and applications in this challenging area, and provides a chance of exchanging ideas on them. This workshop is following the HSCD 2015: Satellite Event of QRS 2015 in Vancouver, Canada, August 3-5, 2015, organized by Professor Donghui Guo, Xiamen University, Xiamen City, China.

Topics of interest

Topics of interest include, but are not limited to, the following areas:

- Co-Synthesis and Automatic Synthesis
- Design Specification and Verification
- Generic Co-Design Methodology
- Partitioning Algorithm and Component Section
- Programmable Component
- Prototyping and Emulation
- Tools and FPGA of HSCD
- Applications and Empirical Studies of HSCD

Submission

All submissions must be in English and original works that have not been published and/or submitted for consideration of publication anywhere else. The format of the submission must follow the guidelines for IEEE conference proceedings (http://www.ieee.org/conferences_events/conferences/publishing/templates.html) with maximum of eight pages. Each submission should also include a 150-word abstract and up to 6 keywords. All accepted papers will be published by IEEE Press and made available in the IEEE digital library as the company of QRS proceedings. Detailed instructions for electronic paper submission and the review process can be found at the QRS16 conference submission website http://paris.uta.edu/qrs16.

Important Dates

- Submission Deadline: April 22, 2016
- Author Notification: May 25, 2016
• Camera-ready Versions: June 10, 2016
• HSCD Workshop: August 1-3, 2015

Program Committee

Program Chair

Yixiang Chen: East China Normal University, China
Robert De Simone: INRIA, France

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General Inquiries

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